

ACKNOWLEDGEMENT RECEIPT

Electronic Version 1.1

Stylesheet Version v1.1.1

Title of Invention

CIRCUIT AND METHOD OF ESTABLISHING DC BIAS LEVELS IN
AN RF POWER AMPLIFIER

Submission Type : Information Disclosure
Statement

Application Number:

10/743221



EFS ID:

78022

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
ICON1	7643
USPTOEFSNot	For assistance with e-filing a patent application, contact the Patent Electronic Business Center: Toll-Free Number:1(866) 217-9197 Website: http://www.uspto.gov/ebc/

First Named Applicant: Alan Westwick

Attorney Docket Number: SIL.P0063

Timestamp: 2005-02-13 19:18:11 EDT

From: US

File Listing:

Doc. Name	File Name	Size (Bytes)	Date Produced (yyyymmdd)
us-ids	SILP63-1-usidst.xml	1477	2005-02-13
us-ids	us-ids.dtd	7763	2005-02-13
us-ids	us-ids.xsl	12026	2005-02-13
package-data	SILP63-1-pkda.xml	1766	2005-02-13
package-data	package-data.dtd	27025	2005-02-13
package-data	us-package-data.xsl	19263	2005-02-13
Total files size		69320	

Message Digest: 058aec16e809afd591a3b6b52beb7ea2a73b06ea

Digital Certificate Holder
Name:

cn=Bruce Alan
Johnson,ou=Registered
Attorneys,ou=Patent and
Trademark
Office,ou=Department of
Commerce,o=U.S.
Government,c=US